

ABSTRACT

The effects of power supply resistance and internal resistance drop are measured at any time in the interior or other strategic areas of an IC device during steady-state operation, and the results of 5 managed internal voltage regulation are fed back from at least one point, but preferably from several or more points throughout the power grid of the IC. Thus, stability of voltage regulation on an IC including at least one integrated voltage regulator is dynamically controlled with voltage regulation that is managed to effectively provide a 'secondary' voltage 10 regulation of the output of one or more voltage regulators to provide a desired voltage output result on one or more portions of a power grid. The management may be implemented in an IC having a singular power grid with respect to a particular supply voltage, or in an IC having multiple power grid structures (e.g., one in each quadrant, etc.)

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